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Scholar

Results 1 - 48 of 48 for dsp integrated circuit "network processor". (0.02 seconds)

Addressing the system-on-a-chip interconnect woes through communication-based design - group of 29 »

M Sgroi, M Sheets, A Mihal, K Keutzer, S Malik, J ... - PROC DES AUTOM CONF. pp. 667-672. 2001, 2001 - doi.ieeecomputersociety.org

... a reconfigurable **integrated circuit** for **DSP** applications that ... and methodologies to allow **integrated circuit** designers to ... Intel **Network Processor** Targets Routers ...

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Hardware-Software Cosynthesis for Digital Systems - group of 9 »

RK Gupta, G De Micheli - IEEE Design & Test of Computers, 1993 - mesl.ucsd.edu

... description languages (HDLs) to describe **integrated** circuits has ... a behavioral description of **circuit** func ... rate systems particularly used for **DSP** applications, ...

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Hardware requirements for neural network pattern classifiers. - group of 5 »

BE Boser, E Sackinger, J Bromley, Y leCun, LD ... - IEEE Micro, 1992 - ieeexplore.ieee.org

... are well matched to **integrated circuit** technology ... processor and **DSP** implementation are very similar at 5.3 ... A special-purpose neural **network processor** must be ...

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Network processors: a perspective on market requirements, processor architectures and embedded SW ...

- group of 12 »

PG Paulin, F Karim, P Bromley - DATE, 2001 - doi.ieeecs.org

... A **network processor** is a highly **integrated** set of ... technology has been applied to the MMDSP+ audio **DSP**. ... the instruction-set simulator and in-**circuit** emulator. ...

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Parallel **DSP** testbed with a heterogeneous and reconfigurable network fabric - group of 7 »

SK Tewksbury, K Devabattini, V Gandikota - PROC ANNU IEEE INT CONF INNOV SYST SILICON. pp. 310-322. ..., 1997 - msrsc.wvu.edu

... Analog Devices SHARC DSPs), with a variety of parallel **DSP** array architectures ... of fine-grained processors **integrated** onto a single **integrated circuit** (ICs) as ...

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An 80/20-MHz 160-mW Multimedia Processor **Integrated** With Embedded DRAM, MPEG-4 Accelerator, and 3-D ... - group of 6 »

CW Yoon, R Woo, J Kook, SJ Lee, K Lee, HJ Yoo - IEEE JOURNAL OF SOLID STATE CIRCUITS, 2001 - ieeexplore.ieee.org

... frame buffers, and peripheral blocks are **integrated** together ... A MPEG-4 programmable codec **DSP** with an ... to network switches and high-speed **circuit** techniques, espe ...

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The Challenge for Next Generation Network Processors. - group of 3 »

I Agere - White Paper, 1999 - irwinlazar.com

... ASICs), Digital Signal Processors (**DSP**) or Reduced ... accounted for by the **network processor** at the ... Application Specific **Integrated Circuit** (ASIC) The application ...

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Programmable Logic IP Cores in SoC Design: Opportunities and Challenges - group of 5 »

SJE Wilton, R Saleh - PROCEEDINGS OF THE IEEE CUSTOM **INTEGRATED** CIRCUITS ..., 2001 - dspwork.com

... more thorough verification job would verify the **circuit** with a ... and as the complexities of **integrated** circuits increase ... of FPGA with IP Cores - **DSP** First Target ...

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Analog neuro-chip with on-chip learning capability for adaptive non-linear equalizers - group of 3 »

JW Cho, SY Lee - Proceedings of the International Joint Conference on Neural ..., 1998 - [ieeexplore.ieee.org](#)

... done on the chip and external **DSP** and Analog-to ... Sigmoid-derivative **circuit**, and an analog multiplier **circuit**. The numbers of **integrated** synapse and neuron cells ...

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Understanding Network Processors - group of 10 »

N Shah - Master's thesis, University of California, Berkeley, Sep, 2001 - [students.iiit.ac.in](#)

... them · Developers and designers in **network processor** related fields ... system implementation: · ASIC (Application Specific **Integrated Circuit**) – any hardwired ...

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Design Approaches to Field-Programmable Analog **Integrated** Circuits - group of 6 »

DR D'Mello, PG Gulak - Analog **Integrated** Circuits and Signal Processing, 1998 - Springer

... analog circuitry in one neural **network processor** [20] was ... eld- programmable analog array **integrated** circuits that ... based on switched-capacitor **circuit** techniques ...

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A Signal Processing System based upon Monolithic Neural Coprocessors - group of 5 »

J Guimaraes, A Romariz, P Ferreira, J Campelo, M ... - SBRN, 1998 - [doi.ieeeecs.org](#)

... a dedicated hardware (a printed **circuit** board containing ... constitute a recompilation of the **integrated** mode and ... ligação em circuitos com **DSP**.(in Portuguese). ...

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Integrated Memory/Network Architectures for Cluster-Organized, Parallel **DSP** Architectures - group of 10 »

SK Tewksbury, V Gandakota, K Devabattini, P ... - IC/Package Design Integration, 1998. Proceedings. 1998 IEEE ..., 1998 - [doi.ieeecomputersociety.org](#)

... Possible Network-**Integrated** Operating System Functions A. Idealized Model ... **Circuit** switched network link Store and forward, packet- switched ... D 4 **DSP** D 5 **DSP** D 6 ...

[Web Search](#)

A Vlsi Neural **Network Processor** Based on Hippocampal Model - group of 5 »

RH Tsai, BJ Sheu, TW Berger - Analog **Integrated** Circuits and Signal Processing, 1998 - Springer

Analog **Integrated** Circuits and Signal Processing, 15 ... A VLSI Neural **Network Processor** Based on Hippocampal ... **Circuit**-level simulation results and experimental data ...

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Soldier Phone: An Innovative Approach to Wireless Multimedia Communications - group of 6 »

R Bittel, E Caples, C Young, F Loso - MILCOM, 1998 - [doi.ieeecomputersociety.org](#)

... This protocol suite has been **integrated** with baseband ... 2. **DSP** and support logic providing preamble matched ... to support both packet and **circuit** switched traffic. ...

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SYNAPSE-X {A General-Purpose Neurocomputer Architecture - group of 2 »

U Ramacher - Proceedings of the IEEE International Joint Conference on ..., 1991 - [ieeexplore.ieee.org](#)

... have to be supported by a specially designed VLSI **circuit** which will ... of a sequence of elementary operations and their optional concatenations with **DSP** or host ...

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An **Integrated** Simulation Environment for Parallel and Distributed System Prototyping - group of 4 »

AD George, RB Fogarty, JS Markwell, MD Miars - SIMULATION-CALIFORNIA-, 1999 - [hcs.ufl.edu](#)

... to integrate software with microchip or **circuit** board simulation ... user's code is **integrated** with a network ... Models In ISE, the **network**, **processor**, and software ...

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Design and optimization of networked wireless information systems - group of 4 »

M Srivastava - IEEE VLSI workshop, 1998 - [ieeexplore.ieee.org](#)

... higher than processor, memory and **DSP** cores, and ... Energy-efficient Radio Linked **Integrated** Network) project ... power optimizations done during **circuit**, logic, and ...

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Fast Timed Cosimulation of HW/SW Implementation of Embedded Multiprocessor SoC Communication -

group of 6 »

S Yoo, G Nicolescu, L Gauthier, AA Jerraya - HLDVT'01: IEEE International Workshop on High Level Design ..., 2001 - ieeexplore.ieee.org

... recently, high-performance and complex communication networks are being **integrated** into the ... a TDMA-style on-chip bus [3]. A **circuit** switch communication ... P **DSP** ...

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[P5] Digital Systems for Neural Networks - group of 4 »

P lenne, G Kuhn - Digital Signal Processing Technology, 1995 - diwww.epfl.ch

Page 1. c1995 Society of Photo-Optical Instrumentation Engineers. This paper was published in P. Papamichalis and R. Kerwin, editors ...

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Recent developments of electronic neural nets in North America - group of 3 »

HP Graf, E Sackinger, LD Jackel - The Journal of VLSI Signal Processing, 1993 - Springer

... of the characters is fixed, a network of about 10,000 connections suffices and can be **integrated** on the same chip with the photosensors. The **circuit** is hard ...

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A Programmable

J Choi, SH Bang, BJ Sheu - IEEE TRANSACTIONS ON NEURAL NETWORKS, 1993 - ieeexplore.ieee.org

... of a compact and wide-range analog multiplier **circuit**. ... The neural **network processor** chip performs the feedforward operation and a companion **DSP** board executes ...

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Analogue neuro-chip with on-chip learning capability for adaptive nonlinear equalisers

D MATHOORASING, S BOUCHOULE, C KAZMJERSKI, E ... - ELECTRONICS LETTERS, 1997 - ieeexplore.ieee.org

... without any help of a host computer or **DSP**. ... ISHIHARA, r., and YASUMOTO, M.: '**Integrated** tapped MOS ... fibre amplifiers and equivalent electrical **circuit** 10 15 ...

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[BOOK] Readings in hardware/software co-design

G De Micheli, R Ernst, W Wolf - 2001 - books.google.com

... andmagazines thatpublish articles on hardware/software co-design computing include • IEEE Transactions on Computer-Aided Design of **Integrated** Circuits and ...

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Mixed analog/digital hardware synthesis of artificial neural networks - group of 3 »

A Achyuthan, MI Elmasry - ... on Computer-Aided Design of **Integrated** Circuits and Systems, 1994 - ieeexplore.ieee.org

... randomness is generally valid for analog VLSI hardware as well, since most of the nonidealities are due to random variations in **integrated circuit** fabrication. ...

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Integrating high-level simulation into a model-integrated embedded system design toolset

BK Eames - 2001 - isis.vanderbilt.edu

... VHDL – Very high speed **integrated circuit** Hardware Description Language ... principles of the model-**integrated** approach to ... TMS320C40 **DSP**, which is connected to a ...

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System-Level Design: Orthogonalization of Concerns and Platform-Based Design - group of 22 »

K Keutzer, S Malik, AR Newton, JM Rabaey, A ... - ... ON COMPUTER AIDED DESIGN OF **INTEGRATED** CIRCUITS AND SYSTEMS, 2000 - ieeexplore.ieee.org

... If one considers an **integrated circuit** (IC) implementation ... COMPUTER-AIDED DESIGN OF **INTEGRATED** CIRCUITS AND ... mapped" into a programmable microprocessor or **DSP**. ...

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Lessons Learned from the Design of a Mobile Multimedia System in the MOBY DICK Project - group of 5 »

GJM Smit, PJM Havinga - LECTURE NOTES IN COMPUTER SCIENCE, 2000 - Springer

... multiprocessor architectures mainly targeted for **DSP** algorithms [eg ... will be achieved in the **integrated** design of ... a narrow speciality in analog- **circuit** design. ...

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Server ATM Integration - group of 2 »

DA Spencer, KW Howard - Bell Labs Technical Journal, 1999 - doi.wiley.com

... multiplexing for ATM IP—Internet protocol ISDN—**integrated** services digital ... PRI—primary rate interface PVC—permanent virtual **circuit** QoS—quality of ...

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Advances in wireless terminals - group of 6 »

P Lettieri, MB Srivastava - IEEE Personal Communications, 1999 - www-2.cs.cmu.edu

... this has taken the form of **circuit**-switched and ... actuators with digital signal processing (**DSP**) processors and ... in the Low Power **Integrated** Wireless Microsensors ...

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[book] Field-Programmable Analog Arrays

E Pierzchala - 1998 - books.google.com

... the area of programmable analog **integrated** circuits is ... the storage of analog **circuit** coefficients on ... analog circuitry in one neural **network processor** [20] was ...

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Digital private branch exchanges - group of 3 »

S Junker, W Noller - IEEE Communications Magazine, 1983 - ieeexplore.ieee.org

... Current PABX technology for **circuit** switched voice and data. ... being used as the generic name for private switching systems having voice or **integrated** voice/data ...

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[book] The computer engineering handbook - group of 3 »

VG Oklobdzija - 2001 - books.google.com

Page 1. | Page 2. THE COMPUTER EER HANDBOOK Page 3. The Electrical Engineering Handbook

Series Series Editor Richard C. Dorf University of California, Davis ...

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Moby Dick, on the design of a Swiss army knife of computing - group of 3 »

PJM Havinga, GJM Smit - International Conference on Advances in infrastructure for ..., 2000 - wwwhome.cs.utwente.nl

... multiprocessor architectures mainly targeted for **DSP** algorithms (eg ... will be achieved in the **integrated** design of ... be a narrow speciality in analog-**circuit** design ...

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Verification Simulation Acceleration Using Code-Perturbation - group of 5 »

B Min, G Choi - Journal of Electronic Testing, 2000 - Springer

... **DSP** product (inte- grated from a **DSP** core and ... related to each other and the **circuit** has very ... Transactions on Computer- Aided Design of **Integrated** Circuits and ...

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A VLSI neuroprocessor for image restoration using analog computing-based systolic architecture - group of 3 »

J Lee, BJ Sheu, R Chellappa - The Journal of VLSI Signal Processing, 1993 - Springer

... Several digital signal processing ICs such as the **DSP**- 56000 chip ... In the prototype neural **network processor** chip design, the synapse **circuit** in figure 6(a ...

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An Analog VLSI Implementation of a Feature Extractor for Real Time Optical Character Recognition - group of 3 »

GM Bo, DD Caviglia, M Valle - IEEE JOURNAL OF SOLID STATE CIRCUITS, 1998 - ieeexplore.ieee.org

... detectors, and of a digital signal processor (**DSP**), ... acts by centered-object **integrated** segmentation and ... A. Colla, "Feature extraction **circuit** for optical ...

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MODELINTEGRATED PROGRAM SYNTHESIS FOR REALTIME IMAGE PROCESSING By Michael S. Moore Dissertation

T Nashville - 1997 - isis.vanderbilt.edu

... 38 MetaLevel Driven Techniques : : : : 38 Model **Integrated** Program Synthesis ... **Integrated** RealTime Image Processing System

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Implementing Regularly Structured Neural Networks on the DREAM Machine

I INTRODUCTION - IEEE TRANSACTIONS ON NEURAL NETWORKS, 1995 - ieeexplore.ieee.org

Page 1 IEEE TRANSACTIONS ON NEURAL NETWORKS. VOL. 6. NO. 2, MARCH 1995

407 1045—9227/95\$04.00 © 1995 IEEE Implementing Regularly ...

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Mobile Multimedia Systems - group of 5 »

PJM Havinga - University of Twente, Ph. D Thesis, 2000 - ub.utwente.nl

... 2.3.4 Technological and **circuit**-level conclusions ... this paradigm, the basic personal computing and communication device will be an **integrated**, battery-operated ...

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A process-independent, 800-MB/s, DRAM byte-wide interface featuring command interleaving and ... - group of 4 »

MM Griffin, J Zerbe, G Tsang, M Ching, CL Portmann - IEEE Journal of Solid-State Circuits, 1998 - ieeexplore.ieee.org

... the development of a digital neural **network processor**. ... microelectromechanical systems and **integrated circuit** design ... designed video decoders and **DSP**'s. Since ...

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[book] Massively Parallel, Optical, and Neural Computing in Japan: A Survey of Major Research Activities

UEDT Wattenberg - 1992 - books.google.com

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Page 2. German National Research Center for Computer Science ...

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THE QUALITY OF VOICE OVER IP - group of 6 »

T Yletyinen - ... , Lab Of Telecommunications TechnologyHelsinki University of ... , 1998 - tct.hut.fi

... DSI Digital Speech Interpolator **DSP** Digital Signal Processor F ... ISDN **Integrated** Services Digital Network ... PVC Permanent Virtual **Circuit** Q QoS Quality of Service ...

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[book] Field programmable logic and applications

RW Hartenstein, H Grunbacher - 2000 - books.google.com

... System Level **Integrated** Circuits ... Revés, X.; Gelonch, A.; Casadevall, F.; Garcia, fL Analysis ofRNS-FPL Synergy for High Throughput **DSP** Applications: Discrete ...

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Evolution of an open communications architecture - group of 3 »

RJ Cypser - IBM Systems Journal, 1992 - research.ibm.com

... data net- works (PSDNS), Broadband **Integrated** Services Digital ... Subnetworks, like x.21 **circuit** switched net ... The IBM 6611 **Network Processor**, for example, is a ...

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Custom embedded counterflow pipelines - group of 2 »

BR Childers - 2000 - cs.virginia.edu

... using either a reconfigurable architecture or application-specific **integrated** circuits ... Perry94] and Verilog [Open93] are geared towards hardware and **circuit** ...

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SIMD Architectures for Radar Signal Processing and Artificial Neural Networks - group of 2 »

M Taveniku - DOKTORSAVHANDLINGAR-CHALMERS TEKNISKA HOGSKOLA, 1997 - ce.chalmers.se

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Networks By Mikael Taveniku Technical Report no. 321 ...

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[book] System design automation: fundamentals, principles, methods, examples

R Merker, W Schwarz - 2001 - books.google.com

... for above mentioned single-chip solutions where memory must be **integrated** on chip ... software partitioning), or for optimal code synthesis for **DSP** processors from ...

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